

Election

In response to the Restriction Requirement in the October 22, 2007 Office Action, Applicants hereby elect Invention I, i.e., claims 23-39 and 42 directed to a signal level displacement circuit for a flip flop.

**REMARKS**

I. Status of the Application

Claims 23-42 are pending in this application. In the October 22, 2007 Office Action, the Examiner restricted claims 23-42 to one of Invention I consisting of claims 23-39 and 42 drawn to a signal level displacement circuit for a flip flop operable to be clocked by a clock signal, Invention II consisting of claim 40 drawn to a method, and Invention III consisting of claim 41 drawn to a flip flop. The Examiner required Applicants to elect one of the three inventions.

In this response, Applicants have elected Invention I, consisting claims 23-39 and 42 directed to a signal level displacement circuit for a flip flop, without traverse.

II. Conclusion

For all of the foregoing reasons, it is respectfully submitted that the elected invention is in a condition for prosecution on the merits. Favorable consideration and allowance of this application is, therefore, respectfully requested.

Respectfully submitted,



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